ECE 2049 LECTURE 8 OFFICE Houses -TODAY: 2-6PA (NICK) - Tonorenous: 3-SPA (RUSHI) TODAY - MORE DIGITAL 1/0 -MONDAY: 3-5 AM (RUSHI) - Jurno TO LAI3 2 ADMINISTRIVIA - NW3: DUE TODAY BY 11:59 PM - YOU CAN RESUBRIT AFTER CLASS IF YOU WANT - NWY: ONLINE AFTER CLASS, DUE TOES BY 2PM - GRADLE FOR NWI-3: THIS WEEKLIND - LABI REMORT: DUE FRI BY 11:59 PM - LABZ: STANTS TODAY - PRELAB QUE BY SIGDOFF OR ONLINE SUBRISSION BY GPM TUESDAY

- EXAM 1: OUT AFTER TUBDAY'S CLASS (WHICH WILL BE REVIEW) - OPEN BOOK /NOTES / INTERNET / CALCULATOR (BUT INDIAL) - DESIGNED FOR 1-2 NRS - DUE TAURS JUNE 16, BY 2PM (START OF CLASS) - TOPICS : NW 1-4, LECTURES 1-8 - NUMBER REPRESENTATIONS - MSP 430 ARCHITECTURE - DIGITAL 10 - DICITAL YO - GLINICAAL CONCLETS FROM LAB

2. BONUS (2 pts): Fun with memory-mapped peripherals: Say you are you using a different microprocessor that exposes the memory bus so that you can add new memory-mapped peripherals to it. Assume that you attach your peripheral device and that it has one 16-bit value that the CPU can read at address 0x1104.

How would you write code to read the value at this address? Like all register definitions in C, you can do this with a single #define statement. Complete the definition below, which includes an example of how the register should be used.

#define MY REG (/* Fill in your definition here! */) void main(void) { int val: val \in MY REG: // Read the value of the peripheral at address 0x1104 // . . .

5, 11 WRITE 5 TO THE ADDROSS WHENE PZDIR IS ON MEMORY BUS 11 GANT JA Ox1104 WRITH to MENORY AT

X= MY_KEG - DEREFERENCE (* (UNSIGNED INT *) (0,1104)

ECE2049-E22

BUTINUNG WI DIGITAL 1/6

Digital I/O Examples

Example 1: Input and output registers

Assume the following digital I/O pins are configured correctly. P3.1-0 are configured as inputs, and P3.7-4 are outputs.

Input Output 93.0 3.5 A Hypothetical Specification: a_1 a_0 Z_3 Z_2 Z_1 Z_0 **Input:** Read a 2-bit binary value *a* on P3.1-0 0 0 0 1 0 0 **Output:** Given *a*, set P3.7-4 based on the table: 0 1 1 0 0 1 0 0 0 0 1 0 1 0 0 1 1 0 1 DECODIN 60612 LEDC P3,1-0 P>7-4 OUR re CODE REAP P3,1-0 FROM INDUT REGISTER (HAR INBITS = P3IN & (BITI (BITO) NEED TO SELECT ONLY BITS 1-0 FROM INPUT REGISTER_ BIT! BITO GOOD OOT I GOOD COXX ONLY BITS 1-0 CAN BE NONZEND

5-9

82 950 11 SET WLL BITS IN OUTPUT (P3.7-4), THEN TURN ON BITS WE NEED. R300to to: POUT 6= ~ (BITT/BITG/BITS/BITY)' PBOUT TX+++ ++++ 70000 1111 0000 ++++ SWITCH (INBITS) EASE O! .P300T /= BITT; 1/ SET P3.7 TO (BREAK, CASE 1: PJOUT = BITG; BREAK; CASE 2: PJOUT 1= BITS; DREAK; CASE 3: P3OUT |= BITY' BREAK' 3

NOW LET'S CONTINUE TO BUILD THIS ECE2049-E22 LEXAMPLE ON ON OUR LAB BOLLO 5-9 (WHICH USES DIFFERENT PINS) **Digital I/O Examples Example 1: Input and output registers** Assume the following digital I/O pins are configured correctly. P3.1-0 are configured as inputs, and P3.7-4 are outputs. SONE Input Output RUTTON P2.1 P1. 16.Z P6.1 86.3 P61 **A Hypothetical Specification:** a_1 a_0 Z_3 Z_2 Z_1 Z_0 **Input:** Read a 2-bit binary value *a* on 0 0 0 0 1 0 **Output:** Given *a*, set P37-4 based on the table: 0 1 1 0 0 1 0 Ky I LEDS 0 0 0 1 0 1 1 1 0 0 0 1 INPUT ~ LOGIC ~ OUTPOR INPUT: LAUNCHPAD BUTTONS MITPUT SOME LEDS BUT HOW DO WE KNOU HOW/ WHERE TO CONNET to PINSC ONNECTING STUFF: KEY STEPS 2.5. (IF INPUT) FIND WHERE NAROWARE (PINS FIND WHERE NAROWARE (PIL-LP/DOWN RESISTORS 1. FIND AVAILABLE PINS IS CONNECTED (P+.Y) REQVIRED 2. INFUT OR OVTPUT? 3. INTERPRET TRE CIRCUIT (WHAT DOES A I OR O MEAN?) SEE DECODER EXAMPLE ON COURSE WEBSITE FOR COMPLETE EXAMPLE U/ NOTES.

Digital I/O Concepts: Input or Output?

How do you know if something is an input or an output?

- If we are "reading" state from a hardware device, it is an input
- If we are "writing" or "setting" the state of a device, it is an **output** •

GET JUE STATE Consider the LEDs on our board. Are they inputs or outputs? What logic level lights the LED



the buttons and LEDs. In these programs, it's a good idea to wrap the functionality for hardware components into useful functions.

See setLeds() in the demo project for an example!

BUTTONS ARE A TYPE OF INPUT WANT JO



COULD ALSO NAVE MANY OTHER CONFIGURATIONS. - VCL= 3.3V WHAT LOGIC FR LEVEL LIGNTS THE LED NOW?? Y LED Ex. 61 V MSPY30 Px. y Jev MSP 430 SIWKS" CURRENT CONDALSO USE A TRANSISTON OR SOMETHING ELSE TO PROVIDE MORE POWER cc =3,31 OV or 3.3/

Dealing with Inputs

As we discussed briefly last lecture, inputs may require some special handling. Consider the buttons on the Launchpad board:







SO WE NAVE \$ SOME ADDITIONS TO THE MARDWARE //W BASE FOR THIS ... (Pr REN) LOOKING AT 1/2 PIN CONCETUALLY ... ONE Vec T PULL -UP Prov PULL-DOWN NEW PARENE SENSE VOLTIE INPU INPUT ON PIN KEG O ON 1 , PxIN OUTAN PIN 5 ----DIRECTION 1BIT FUNCTION REGISTER OF STURAGE SULLET REGISTER PYDIR "OUT PUT RELISTER Px SEL GON 1 Prout OTHER HARDWARE (NOT DIGITAL 1/0)

Digital I/O Registers (cont.)

Pull-up/Pull-Down Resistor Enable (PxREN)

Activates pull-up or pull-down resistors when a pin is configured as a digital input.

1. ENJER PULL-UPIDOWN RESISTOR O. INTERNAL RESISTOR IS DICABLED

What controls whether to use a pull-up or pull-down resistor? The output register (PxOUT) is actually re-used for this purpose! Set the appropriate bits to 1 for pull-up resistors, and to 0 for pull-down. See p. 408 of the user's guide for details.

You will also see one more Digital I/O register...

Drive Strength (PxDS)

Controls "drive strength", or amount of current that is sourced from the pin when used as an output. We will always use the default setting for this.

Set to 0 = Reduced drive strength (default) Set to 1 = Full drive strength

Important to note: all I/O pins have *limits* on the amount of current that can pass through them (usually on the order of milliamps). See the MSP430F5529 datasheet for details.

OSUALLY & IOMA

As an embedded developer, it's always important to remember the requirements of the hardware as well as the software!

Example: Launchpad Buttons (cont.)



We can configure these buttons as inputs and using pull-up resistors, as follows:

Note that the buttons are on different ports, so we need to configure them separately!

// Read buttons S2 and S1 and return their state in the // lower two bits of the return value such that // ret = 0 0 0 00 0 S2 S1 unsigned char readButtonsLecture(void) { SEE READLAUNCHPAD BATTONS () IN PERIPHERALS. C IN LAB 2 TEMPLATE.

Polling

How can you monitor and use your properly-configured digital I/O functions?

- ... by repeatedly checking if the button status has changed!
- Since this just involves reading a memory address, it is very fast to execute (on the order of microseconds!)

Example:

```
// Inside your demo project...
while(1)
{
    ret_val = readButtons();
    setLeds(~retVal);
}
```

Another, similar example:

```
ret_val = 0x0f; // Default value for all buttons unpressed
while(ret_val == 0x0f)
{
    ret_val = read_buttons();
}
setLeds(~ret_val);
```

Without a delay, this loop executes in microseconds!

This process is called **polling**—we constantly check the buttons and do something when they change.

At the moment, it's all the program needs to do, so it's fine. But what if we wanted to perform more tasks? What if we wanted the processor to sleep while it was waiting?