

Module 5. Digital I/O

Topics

- More Digital I/O

About Digital I/O

Why do we use Digital I/O anyway?

Digital I/O is a method of directly inputting our outputting logic levels to the pins of the MSP430 Package.

You can use this functionality to implement almost anything!

- Simple devices: Buttons and LEDs
- Control signals for complex peripherals
- ... and more!

1/10

0 → "LOGIC 0"
"LOGIC ~~HIGH~~"
LOW → 0V

1 → "LOGIC 1"
"LOGIC HIGH" → 3.3V (FOR US)

Fun Facts about Digital I/O

- Eight independent, individually-configurable ports, named P1-P8
- Ports 1-7 each have 8 configurable *pins*, and are thus 8 bits wide; Port 8 is 3 bits wide
Pins are referenced as P<port>.<pin>, eg. P1.4. P1.4 => Port 1, Pin 4
- Each pin of each port can be configured individually as input or output
- Most digital I/O pins share physical *package pins* with some other function on the device. This is called *pin multiplexing*.
- Each port is controlled by six single-byte registers

What is a register, anyway?

Register:

CIRCUIT SIMILAR TO MEMORY, BUT
W/ SPECIAL FUNCTIONALITY RELATED TO HARDWARE.

- Registers have addresses just like standard memory, so you can read and write to them
- Provide interface between hardware and software:
 - Reading from a register can get information about the hardware
 - Writing to a register can change how the hardware is configured, or send information to a component
- Functionality provided is defined by the hardware's design. When TI designs the MSP430, they define what registers are exposed to the programmer, which defines the functionality available on the chip.
- All the I/O port registers are *memory-mapped*: each register associated with a digital I/O port has a unique address in memory
 - How do you know what the addresses are? These are defined in the MSP430F5529 datasheet, as well as msp430.h and msp430f5529.cmd

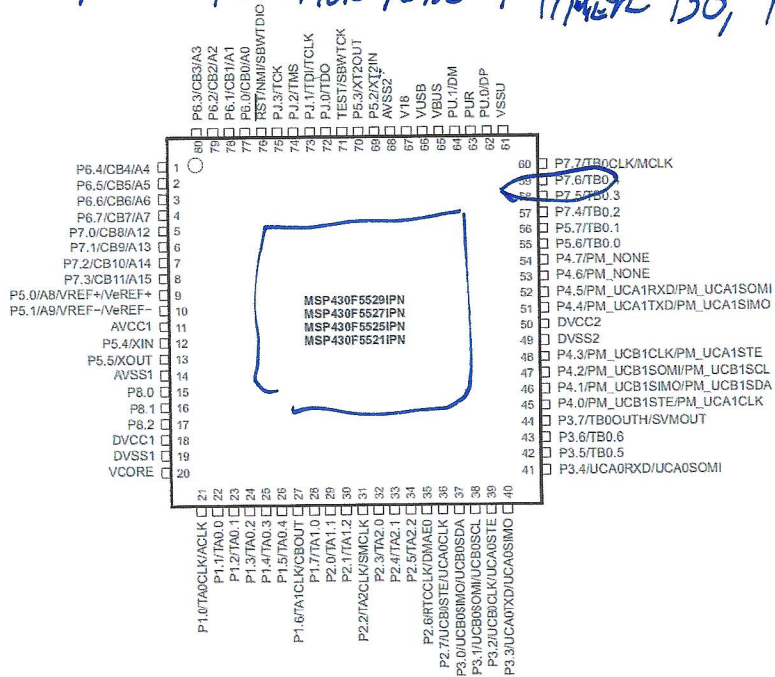
Pins on the Microcontroller

Microcontrollers often pack lots of functionality in a small IC. However, the usage of all this functionality is limited by the physical pins on the IC package:

PIN 59. P7.6/TB0.4

TWO FUNCTIONS

- 1. I/O MODE ⇒ PORT 7, PIN 6
- 2. "FUNCTION MODE": TIMER B0, INPUT 4



In order to maximize the usage of physical pins, most physical pins (also called "package pins") are shared between multiple device functions.

⇒ PIN MULTIPLEXING.

- ONE PART CAN OFFER MORE FUNCTIONALITY THAN THERE ARE PINS

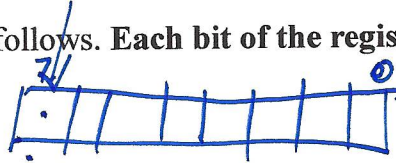
UP TO DESIGNER TO PICK FUNCTIONALITY.

Digital I/O Registers

P37

The 6 registers controlling the digital I/O ports are as follows. Each bit of the register controls the state for a specific pin.

P3SEL



Function Select Register (PxSEL)

Selects the port pin for Digital I/O—remember multiplexing? This selects the function used on the pin.

0: ~~ANALOG~~ SELECT DIGITAL I/O MODE FOR PIN
 1: SELECTING "FUNCTION MODE"

Direction Register (PxDIR)

Sets port pins as Input or Output

Set to 1 = Output

Set to 0 = Input

Ex. P5DIR = 0x0F;
 P5.7-4 ARE INPUTS
 P5.3-0 ARE OUTPUTS

Control Registers

Input Register (PxIN)

This is where the value input on the port appears (this is where you "read" the port)

IF PIN IS AN INPUT

CHAR v = P3IN;

READ INPUT REG STORE INTO V.

DATA REGISTERS

Output Register (PxOUT)

This is where data to be output on the port should be "written"

IF PIN IS AN OUTPUT:

P3OUT = X;

0x00;

Drive Strength (PxDS)

Pull-up/Pull-Down Resistor Enable (PxREN)

We will discuss these two (using examples) later.

Conceptually, once you know which registers to use, using Digital I/O is pretty simple—all you need to do is read or write the desired values to the registers.

Configuration Example

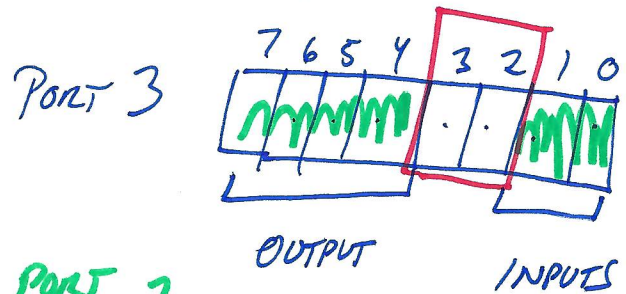
Example: Configure Port 3 for Digital I/O with pins 1 and 0 as inputs and pins 7-4 as outputs.

There are two ways we can approach this problem:

STEPS:

1. SET PINS FOR DIGITAL I/O \Rightarrow P3SEL

2. SET PINS AS INPUT OR OUTPUT \Rightarrow P3DIR



P3SEL = 0₇ 0₆ 0₅ 0₄ 1₃ 1₂ 0₁ 0₀ // ALL PORT 3 DIGITAL I/O

P3DIR = 0xFO // 1111 0000

IN THIS CASE, PINS 3-2 ARE OVERWRITTEN, BUT THEY MIGHT BE USED FOR OTHER FUNCTIONS

\therefore SHOULD PRESERVE VALUE OF PINS WE ARE NOT USING.

Important Background: Bitwise manipulation

Because each bit in a register can control a different pin, we will make extensive use of C's *bitwise* operators (&, |, ~) to manipulate registers.

This is a very common practice when interacting directly with hardware!

Recall the truth tables for the bitwise operators AND (&), OR (|) and NOT (~):

A	B	Z = A & B
0	0	0
0	1	0
1	0	0
1	1	1

A	B	Z = A B
0	0	0
0	1	1
1	0	1
1	1	1

A	C = ~A
0	1
1	0

$X \text{ AND } 0 = 0$ $X \text{ OR } 1 = 1$
 $X \text{ AND } 1 = X$ $X \text{ OR } 0 = X$

Where "X" is either 0 or 1

CLEAR

SET

From these operators, we can build a set of techniques for individually controlling specific bits in a variable while leaving the others unmodified.

Common operations using bitwise operators

Setting individual bits to 1

We can do this by OR'ing a specific bit (or bits) with a 1. This is called "setting" a bit.

Setting individual bits to 0

We can do this by AND'ing a specific bit (or bits) with a 0. This is called "clearing" a bit.

SET BITS (SET TO 1)

$$VAR = VAR | 0x03$$

VAR	0101	0101
1	0000	0011
<hr/>		
	0101	0111

0101 0111
 PRESERVED SET TO 1

CLEARING BITS (SET TO 0)

$$VAR = VAR \& 0x0F$$

VAR	1100	1100
0x0F	0000	1111
<hr/>		
	0000	1100

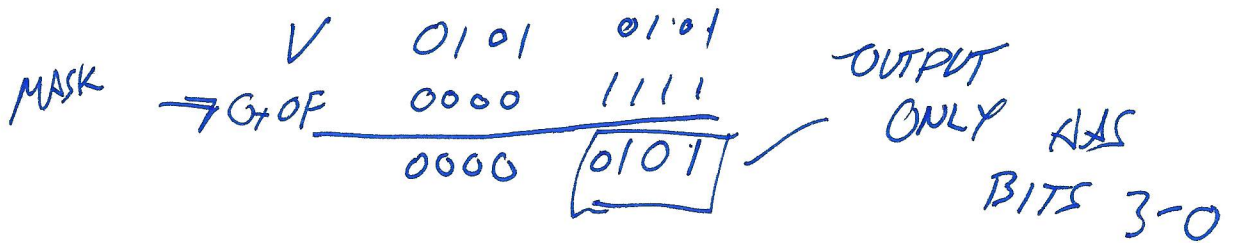
0000 1100
 SET TO 0 SAME AS BEFORE

"Selecting" specific bits from a variable

It is often necessary to check if certain bits of a field are set, or to only take the value of certain bits from a variable. We can do this by AND'ing a variable with only those bits that interest us set to 1—this is called *masking* bits.

READ BITS 3-0 OF V

$$\text{CHAR } x = V \& 0x0F$$



You will use these techniques very frequently when working with digital I/O:

A SLIGHTLY BETTER WAY

PORT 3, PINS 7-4 OUTPUTS
 PORT 3, PINS 1-0 INPUTS

	7	6	5	4	3	2	1	0
P3SEL	x	x	x	x	x	x	x	x
	0	0	0	0	1	1	0	0
	0	0	0	0	x	x	0	0

// SELECT FOR DIGITAL I/O

$$P3SEL = P3SEL \& \underline{0x0C};$$

// SET BITS 7-4 AS OUTPUTS
 (SET TO 1)

$$P3DIR = P3DIR | 0xF0;$$

P3DIR	x	x	x	x	x	x	x	x
	1	1	1	1	0	0	0	0
	1	1	1	1	x	x	x	x

// SET BITS 1-0 AS INPUTS
 (SET TO 0)

$$P3DIR = P3DIR \& 0xFC;$$

P3DIR	x	x	x	x	x	x	x	x
	1	1	1	1	1	1	0	0
	x	x	x	x	x	x	0	0

NEED TO DO THESE IN SEPARATE STEPS!

An even better way: Lose the "magic numbers"

In this lecture, it's clear what the constants 0xF0 and 0xFC mean, but will you remember what's happening here 6 months from now? Probably not.

In C, as in many programming languages, it's good practice to avoid *magic numbers*, or hard coded numbers that appear in the code without explanation of their meaning or purpose. Instead, we can use constants to attach meaning to these values and allow them to be reused.

In this case, a set of constants for the individual bits are defined for us, we can just use them:

Name	Hex	Binary	Name	Hex	Binary
BIT0	0x01	0000 0001b	BIT4	0x10	0001 0000b
BIT1	0x02	0000 0010b	BIT5	0x20	0010 0000b
BIT2	0x04	0000 0100b	BIT6	0x40	0100 0000b
BIT3	0x08	0000 1000b	BIT7	0x80	1000 0000b

We can also combine these constants to refer to more than one bit:

~~0x06~~ ~~0x03~~
 Ex. (BIT2 | BIT1)

0000	00100	BIT2
10000	0010	BIT1
0000		0110

$$\sim(\text{BIT2} | \text{BIT1}) = \sim(0000\ 0110) = 1111\ 1001$$

SO INSTEAD OF P3DIR = ~~0x06~~ P3DIR & 0xF9
 COULD WRITE:

$$P3DIR \& = \sim(\text{BIT2} | \text{BIT1})$$

$$\uparrow P3DIR = P3DIR \& \dots$$

Digital I/O Examples

Example 1: Input and output registers

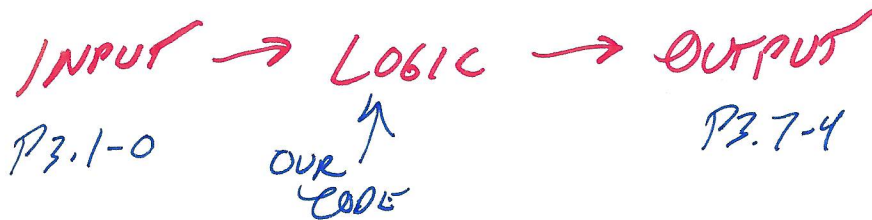
Assume the following digital I/O pins are configured correctly. P3.1-0 are configured as inputs, and P3.7-4 are outputs.

A Hypothetical Specification:

Input: Read a 2-bit binary value a on P3.1-0

Output: Given a , set P3.7-4 based on the table:

Input		Output			
P3.1	P3.0	P3.7	P3.6	P3.5	P3.4
a_1	a_0	z_3	z_2	z_1	z_0
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1



"6.1 6.0 DECODER"

~~CHA~~

INPUT
READ ~~P3.7-4~~ P3.1-0 FROM INPUT REG

CHAR INBITS = P3IN ~~&~~ (BIT1 | BIT0);

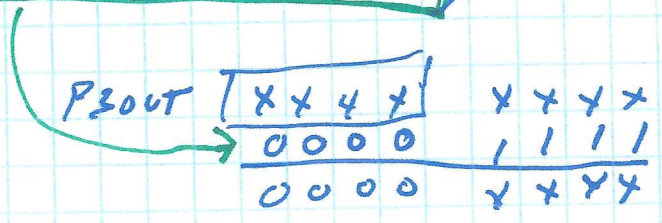
OUTPUT \rightarrow

// SET ALL BITS IN OUTPUT (P3.7-4),
THEN TURN ON BITS WE NEED.

~~P3OUT~~

$$P3OUT \oplus = \sim (BIT7/BIT6/BIT5/BIT4)$$

SWITCH (INBITS)
5



CASE 0:
P3OUT |= BIT7; // SET P3.7 TO 1
BREAK;

CASE 1:
P3OUT |= BIT6;
BREAK;

CASE 2:
P3OUT |= BITS;
BREAK;

CASE 3:
P3OUT |= BIT4;
BREAK;

3

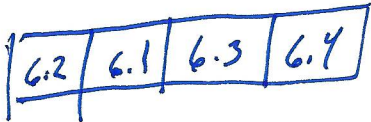
Digital I/O Concepts: Input or Output?

How do you know if something is an input or an output?

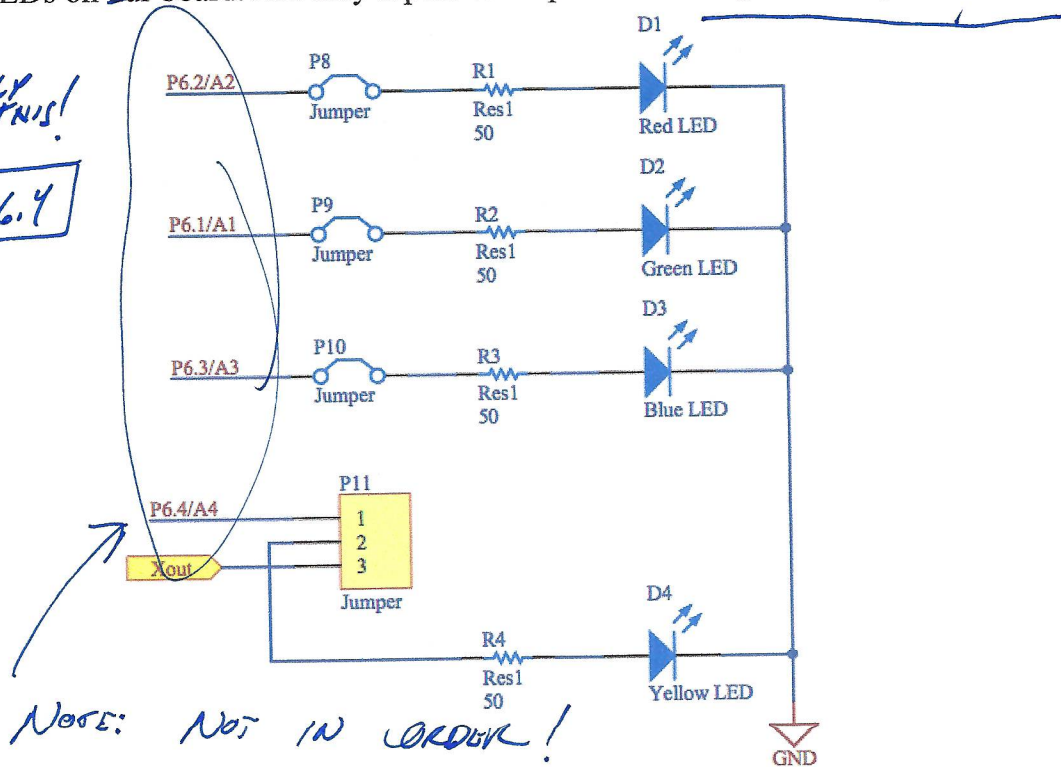
- If we are “reading” state from a hardware device, it is an input
- If we are “writing” or “setting” the state of a device, it is an output

Consider the LEDs on our board. Are they inputs or outputs? What logic level lights the LED?

*ON THIS BOARD,
LEDS PHYSICALLY
ARRANGED LIKE THIS!*



*THIS IS VERY
CONFUSING!*



NOTE: NOT IN ORDER!

HW/SW INTERFACE:

*- WANT TO USE FUNCTIONALITY
W/O ~~CHANGING~~ CARRYING ABOUT PINS
WHEN BUILDING PROGRAMS!*

In an application program like our demo project, we use the digital I/O ports repeatedly to use the buttons and LEDs. In these programs, it's a good idea to *wrap* the functionality for hardware components into useful functions.

See `setLeds()` in the demo project for an example!

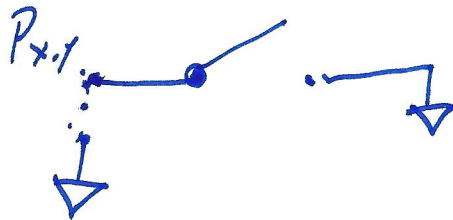
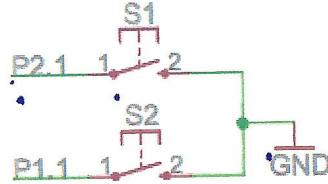
`SET(LUNGNPADLEDS())`

Dealing with Inputs

As we discussed briefly last lecture, inputs may require some special handling.

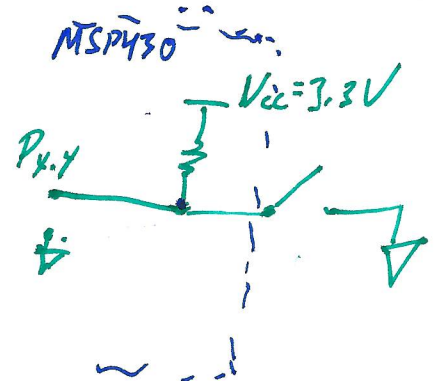
Consider the buttons on the Launchpad board:

User Buttons



FOR THIS CONFIGURATION, INPUT IS UNDEFINED (FLOATING) WHEN SWITCH IS OPEN!

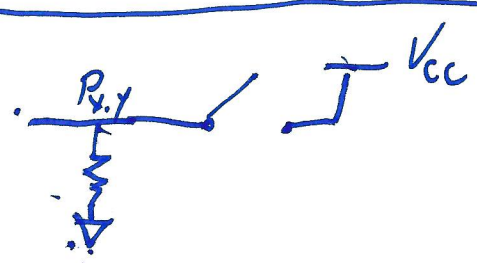
FOR THIS CASE, USE A PULL-UP RESISTOR TO SET A DEFAULT STATE FOR PIN.



WHEN SWITCH IS OPEN $\Rightarrow 3.3V \Rightarrow '1'$
WHEN SWITCH IS CLOSED $\Rightarrow 0V \Rightarrow '0'$

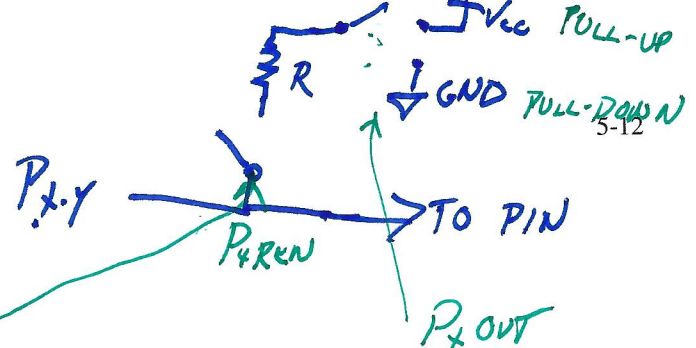
WHY?

DON'T NEED TO INCLUDE PULL-UP RESISTORS ON PCB \Rightarrow LOWER COST.



COULD ALSO WIRE AN INPUT IN A PULL-DOWN CONFIGURATION, LIKE THIS.

Digital I/O Registers (cont.)



Pull-up/Pull-Down Resistor Enable (PxREN)

Activates pull-up or pull-down resistors when a pin is configured as a digital input.

1: ENABLE PULL-UP/PULL-DOWN RESISTOR

0: INTERNAL RESISTOR DISABLED. (DEFAULT)

What controls whether to use a pull-up or pull-down resistor?

The output register (PxOUT) is actually re-used for this purpose!

Set the appropriate bits to 1 for pull-up resistors, and to 0 for pull-down. See p. 408 of the user's guide for details.

You will also see one more Digital I/O register...

Drive Strength (PxDS)

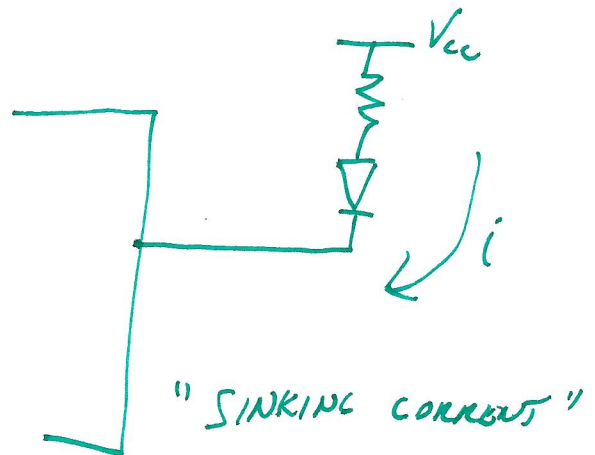
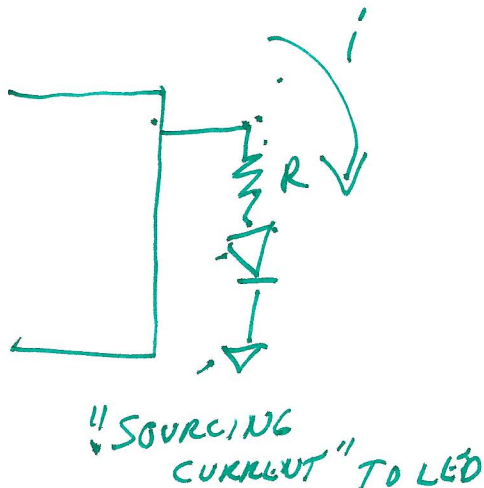
Controls "drive strength", or amount of current that is sourced from the pin when used as an output. We will always use the default setting for this.

Set to 0 = Reduced drive strength (default) Set to 1 = Full drive strength

Important to note: all I/O pins have *limits* on the amount of current that can pass through them (usually on the order of milliamps). See the MSP430F5529 datasheet for details.

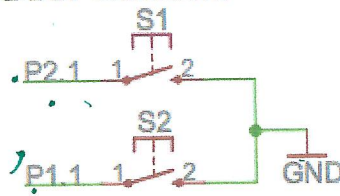
USUALLY: ~10mA

As an embedded developer, it's always important to remember the requirements of the hardware as well as the software!



Example: Launchpad Buttons (cont.)

User Buttons



We can configure these buttons as inputs and using pull-up resistors, as follows:

```
void initButtonsLecture(void)
{
    // Configure buttons as outputs using internal pull up resistors
    // Button 1: P1.1
    P1SEL &= ~BIT1;
    P1DIR &= ~BIT1;
    P1REN |= BIT1;
    P1OUT |= BIT1;
    // Button 2: P2.1
    P2SEL &= ~BIT1;
    P2DIR &= ~BIT1;
    P2REN |= BIT1;
    P2OUT |= BIT1;
}
```

← SELECT FOR DIGITAL I/O

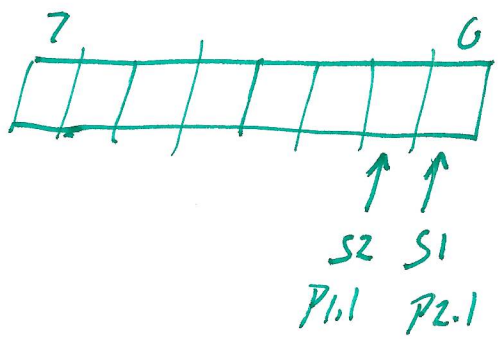
← INPUT

← CONFIGURE FOR PULL-UP

Note that the buttons are on different ports, so we need to configure them separately!

```
// Read buttons S2 and S1 and return their state in the
// lower two bits of the return value such that
// ret = 0 0 0 0 0 0 S2 S1
unsigned char readButtonsLecture(void)
{
    READ LAUNCHPAD BUTTONS ()
}
```

- READ P2IN → P2.1
P1IN → P1.1



Polling

How can you monitor and use your properly-configured digital I/O functions?

- ... by repeatedly checking if the button status has changed!
- Since this just involves reading a memory address, it is very fast to execute (on the order of microseconds!)

Example:

```
// Inside your demo project...
while(1)
{
    ret_val = readButtons();
    setLeds(~retVal);
}
```

Another, similar example:

```
ret_val = 0x0f; // Default value for all buttons unpressed
while(ret_val == 0x0f)
{
    ret_val = read_buttons();
}
setLeds(~ret_val);
```

Without a delay, this loop executes in microseconds!

This process is called **polling**—we constantly check the buttons and do something when they change.

At the moment, it's all the program needs to do, so it's fine. But what if we wanted to perform more tasks? What if we wanted the processor to sleep while it was waiting?